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ELECTRONICS AND COMMUNICATION ENGINEERING DEPARTMENT

A Report on

One Day Workshop on "Application Development using C6000 Digital Signal Processor"

OBJECTIVES OF THE PROGRAMME:

The Purpose of the workshop is to get introduced to DSP Platforms - C5000,C6000, and C2000.

- To Explore C6000 DSP platform featureing TMS 320C6748
- Working with Code Composer Studio for DSP platform.
- To introduce basic board development procedure.
- Working with command file and Gel Files etc.
- Programming techniques in C language and CCS
- DSP activities like basic signal/audio/speech/image and video processing.

EXPERT SPEAKERS:

Name: Mr. Ketan K. Patel

Designation: Sr. Embedded Engineer

Company: Edutech Learning Solutions Pvt. Ltd.

Experience: Total 12 years

PARTICIPANT DETAILS: ANNEXURE-I

Total 93 Participants had participated from final year students of different engineering Colleges.

OUTCOME OF THE WORKSHOP:

- 1. The workshop was divided in two sessions:
 - a. : Introduction to DSP Platform C5000,C6000,C2000& C6000 DSP platform featuring TMS320C6748
 - b. : Working with Code Composer Studio for DSP, Working with Code Composer Studio for DSP Platform, Working with command file and Gel Files and Programming techniques in C Language using CCS.
- Mr. Ketan K. Patel explained the introduction to DSP platforms C6000,C6000 and C2000.
 He also demonstrated the working with Code Composer Studio for DSP platform. He also demonstrated the working with command file and Gel files and Programming techniques in C language using CCS.

The sessions were interactive and the speaker answered the queries of the participants in best possible manner.

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PARTICIPANT DETAILS: ANNEXURE I
WORKSHOP SCHEDULE: ANNEXURE II
WORKSHOP PHOTOGRAPHS: ANNEXURE III
WORKSHOP BANNER: ANNEXURE IV

WORKSHOP CERTIFICATES AND PARTICIPANT FEEDBACKS: ANNEXURE V

PARTICIPANT DETAILS: ANNEXURE-I

Sr.No	Name	Sr.No	Name
1	PACHORE KAMINI M.	48	KRUPAXI KAKADIYA
2	MANALI PATIL	49	WAGH DIPIKA RANCHODBHAI
3	MUKESH RAJBHAR	50	AKSHADA
4	PRIYANKA PADALIYA	51	ISHANT SONI
5	PINKI SHRI KAUSHAL GUPTA	52	GOHIL AJAYKUMAR MAHENDRABHAI
6	JADHAV TEJASVINI SUDHAKAR	53	SALMAN ANSARI
7	PATIL DARSHAN VIJAYBHAI	54	YADAV VISHAL BHARATBHAI
8	LILADHAR BHARAT PATIL	55	KINJAL PATEL
9	JIGYASHA SINGH	56	MISTRY NISHA
10	MEHTA HEMANK BHADRESHBHAI	57	PRAJAKTA RAUT
11	NIDHI SHAH	58	NEHA SHARMA
12	MOHINI MANSUKHBHAI SAVALIYA	59	SUNIL RANA
13	TAPAN KUMAR SINGH	60	DARSHAN
14	ROHIT RAJKUMAR YADAV	61	SOHAM RANA
15	YADAV RAMESH RAMADHAR	62	MOHAMMED MANSURI
16	CHANDANSINGH DINESHSINGH RAJPUROHIT	63	KRUNAL MESHIYA
17	PATEL KHUSHBU B	64	PATEL TEJAS MUKESHBHAI
18	AMIT N YADAV	65	VIVEK NARIYA
19	PATEL TEERTH PARESH	66	GARANA SHAQLAIN
20	KHUSHAL KANANI	67	HARDIK VAGHANI
21	JIGAR SAPKALE	68	YAGNESH RAJESHBHAI GARUD
22	ROHAN PATEL	69	NIL VYAS
23	LEENA GALLE	70	KAILAS G. NAKUM
24	SURESH CHAURASIA	71	UMARETIYA MADHAVI ASHVINBHAI
25	SALONEE DHAGE	72	HIMANI CHHATRIWALA
26	DIGPALSINH PARMAR	73	BHARDAVA MITAL M.
27	VARUN PRADHAN	74	SAMPADA PATIL
28	SHAHIL CHANGANI	75	BHILAVALA DHARMESH R.
29	DOBARIYA DAKSH RAJESHBHAI	76	RAVI KOLADIYA
30	BHAVIN SORATHIYA	77	PINKESH PATEL
31	NILU AMARJEET YADAV	78	DHANRAJ R. CHOUDHARY
32	SINGH PRIYANKA	79	ANIKET MANOJBHAI CHAUHAN
33	ASHWINI TUKARAM GAYAKWAD	80	ANKIT CHUDASAMA
34	ANKITA NARAYANBHAI ROHIT	81	RIYA CHHABDA
35	AKSHAY RANPISE	82	VAIDEHI PAINTER
36	BALAS BHAUTIK HARESH BHAI	83	URVASHI PAWAR



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37	KHAMBHATI ASRAR M.HANIF	84	AHMED
38	ANVI PATEL	85	JAIMISH CHAUHAN
39	VRUTI THAKKAR	86	HARSH M VEKARIYA
40	MAHFUZA MONDAL	87	RAJANIK
41	ADITYA KUMAR PANDEY	88	POOJASHIROYA
42	YADAV YOGESH S	89	KEYUR AASHISHBHAI SHAH
43	CHAUHAN KARAN	90	POPAT PARTH
44	AJAY TIWARI	91	NAITIK SUTARIYA
45	NIKHIL BHIKADIYA	92	HIMANI VAGHANI
46	RAVI PATHAR	93	BHANDERI RUTA RAMJIBHAI
47	PATEL NILANJ PRAVINBHAI		

ANNEXURE II WORKSHOP SCHEDULE

Time	Session	Location
9:30 to 10:00 am	Registration & Breakfast + Tea	Ground floor – Workshop Building
10:00 to 10:30am	Inaugural	Workshop Seminar Hall
10:30 to 12:00 pm	Introduction DSP Platform C5000, C6000, C2000.	Workshop Seminar Hall
12:00 to 1:00 pm	Lunch	Ground floor – Workshop Building
1:00 to 3:0 pm	Working with CCS for DSP Platform	Workshop Seminar Hall
3:00 to 3:15pm	Tea Break	Ground floor – Workshop Building
3:15 to 4:30pm	Programming Techniques in C language using CCS	Workshop Seminar Hall
4:30 to 5:00 pm	Valedictory	Workshop Seminar Hall



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ANNEXURE III- PHOTOGRAPHS

Session: 1 Inaugural & Theory Session









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Session 2 Demonstration





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ANNEXURE IV WORKSHOP BANNER



S. N. Patel Institute of Technology & Research Centre, Umrakh



(A Vidyabharti Trust Institution)

ELECTRONICS & COMMUNICATION ENGINEERING DEPARTME

Organizes

One Day Workshop

Application Development using

C6000 Digital Signal Processor

Date: 24th February, 2020

Coordinators

Dr. Vipul H. Mistry Prof. Zaid M. Shaikhji **Committee Members**

Prof. Virang R. Patel Prof. Hiren A. Tailor Prof. Khushbu B. Karanjiya Prof. Foram B. Patel Prof. Mayank A. Patel

Vidyabharti Campus, At & Po Baben, Ta: Bardoli, Dist: Surat, Pin: 394601

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WORKSHOP CERTIFICATES AND PARTICIPANT FEEDBACKS: ANNEXURE V

A. CERTIFICATE DISTRIBUTION





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B. FEEDBACK BY PARTICIPANTS

*	Shri SitarambhaiNaranji Patel Institute o One day "Application Development usi 24" Fel Orga Electronics and Communici Feedb	Works ing C60 oruary inized ration	shop on 000 Dig , 2020 by Engine	ital Sign	nal Proc	cessor"	mrakh	
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	Present Institute Name: (Kovennent	Ergin	neenim*	Coll	ege	Bhanuch		
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c) Responsive to participants' questions	1	2	3	(4)	
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COORDINATOR CERTIFICATES

